

LAMPIRAN

Data Sheet Komponen IC yang Terdapat pada Rangkaian :

1. IC 14024

7-Stage Ripple Counter

The MC14024B is a 7-stage ripple counter with short propagation delays and high maximum clock rates. The Reset input has standard noise immunity, however the Clock input has increased noise immunity due to Hysteresis. The output of each counter stage is buffered.

- Diode Protection on All Inputs
- Output Transitions Occur on the Falling Edge of the Clock Pulse
- Supply Voltage Range = 3.0 Vdc to 18 Vdc
- Capable of Driving Two Low-power TTL Loads or One Low – power Schottky TTL Load Over the Rated Temperature Range
- Pin-for-Pin Replacement for CD4024B

MAXIMUM RATINGS (Voltages Referenced to V_{SS}) (Note NO TAG)

Symbol	Parameter	Value	Unit
V_{DD}	DC Supply Voltage Range	-0.5 to +18.0	V
V_{in}, V_{out}	Input or Output Voltage Range (DC or Transient)	-0.5 to $V_{DD} + 0.5$	V
I_{in}, I_{out}	Input or Output Current (DC or Transient) per Pin	± 10	mA
P_D	Power Dissipation, per Package (Note NO TAG)	500	mW
T_A	Ambient Temperature Range	-55 to +125	$^{\circ}C$
T_{stg}	Storage Temperature Range	-65 to +150	$^{\circ}C$
T_L	Lead Temperature (8-Second Soldering)	260	$^{\circ}C$

This device contains protection circuitry to guard against damage due to high static voltages or electric fields. However, precautions must be taken to avoid applications of any voltage higher than maximum rated voltages to this high-impedance circuit. For proper operation, V_{in} and V_{out} should be constrained to the range $V_{SS} \leq (V_{in} \text{ or } V_{out}) \leq V_{DD}$. Unused inputs must always be tied to an appropriate logic voltage level (e.g., either V_{SS} or V_{DD}). Unused outputs must be left open.

PIN ASSIGNMENT



2. IC 14042

Quad Transparent Latch

The MC14042B Quad Transparent Latch is constructed with MOSP-channel and N-channel enhancement mode devices in a single monolithic structure. Each latch has a separate data input, but all four latches share a common clock. The clock polarity (high or low) used to strobe data through the latches can be reversed using the polarity input. Information present at the data input is transferred to outputs Q and \bar{Q} during the clock level which is determined by the polarity input.

When the polarity input is in the logic “0” state, data is transferred during the low clock level, and when the polarity input is in the logic “1” state the transfer occurs during the high clock level.

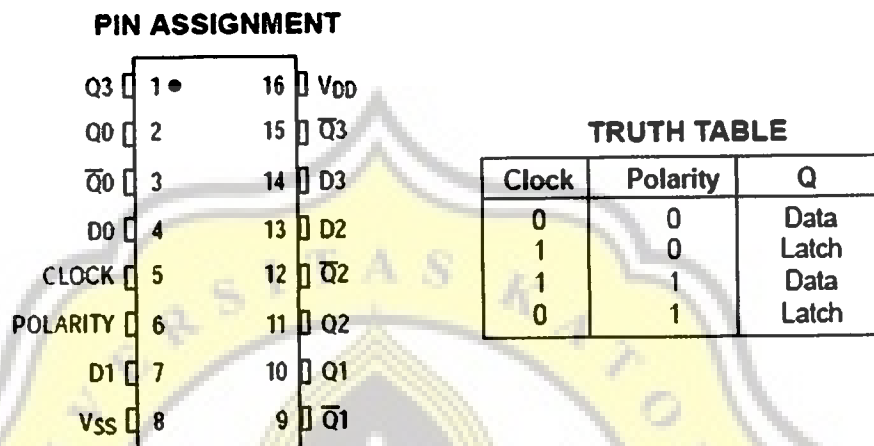
- Buffered Data Inputs
- Common Clock
- Clock Polarity Control
- Q and Q Outputs
- Double Diode Input Protection
- Supply Voltage Range = 3.0 Vdc to 1.8 Vdc
- Capable of Driving Two Low-power TTL Loads or One Low-power Schottky TTL Load Over the Rated Temperature Range

MAXIMUM RATINGS (Voltages Referenced to V_{SS}) (Note 2.)

Symbol	Parameter	Value	Unit
V_{DD}	DC Supply Voltage Range	-0.5 to +18.0	V
V_{in}, V_{out}	Input or Output Voltage Range (DC or Transient)	-0.5 to $V_{DD} + 0.5$	V
I_{in}, I_{out}	Input or Output Current (DC or Transient) per Pin	± 10	mA
P_D	Power Dissipation, per Package (Note 3.)	500	mW
T_A	Ambient Temperature Range	-55 to +125	$^{\circ}C$
T_{stg}	Storage Temperature Range	-65 to +150	$^{\circ}C$
T_L	Lead Temperature (8-Second Soldering)	260	$^{\circ}C$

This device contains protection circuitry to guard against damage due to high static voltages or electric fields. However, precautions must be taken to avoid applications of any voltage higher than maximum rated voltages to this high-impedance circuit. For proper operation, V_{in} and V_{out} should

be constrained to the range $V_{SS} _ (V_{in} \text{ or } V_{out}) _ V_{DD}$. Unused inputs must always be tied to an appropriate logic voltage level (e.g., either V_{SS} or V_{DD}). Unused outputs must be left open.



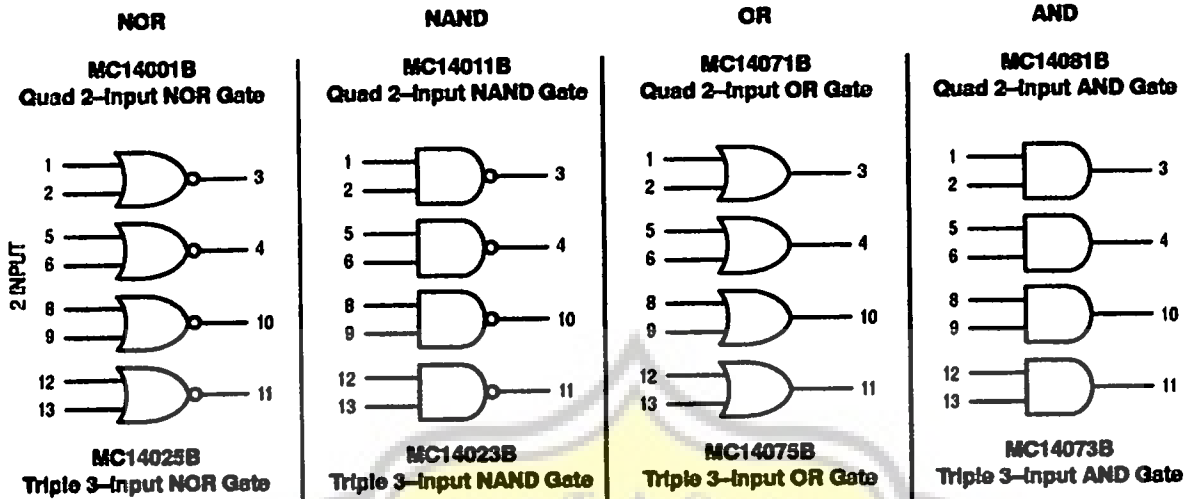
3. IC 14001

B-Suffix Series CMOS Gate

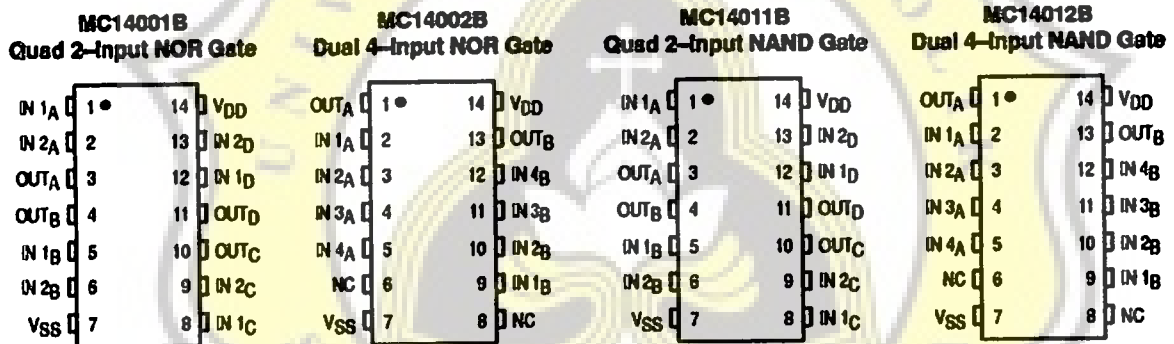
The B series logic gates are constructed with P and N channel enhancement mode device in a single monolithic structure. The primary use in where low power dissipation and high noise immunity is desired.

- Supply voltage range = 3.0 Vdc to 18 Vdc
- All outputs buffered
- Capable of driving two low-power TTL loads or one low-power schottky TTL load over the rated temperature range
- Double diode protection on all inputs except: triple diode protection on MC14011B and MC 14081B
- Pin-for-pin replacements for corresponding CD4000 series B suffix device (exceptions : MC14068B and MC 14078B)

LOGIC DIAGRAMS



PIN ASSIGNMENTS



Specification Pemancar Kamera CCTV (Video Sender) :

VIDEO SENDER



OPERATING INSTRUCTIONS

Thank you for using Wireless VIDEO SENDER. This leaflet contains complete instructions for the installation and operation of VIDEO SENDER. It describes features and the location and use of controls. Read all materials enclosed before attempting to operate the unit.

GENERAL INFORMATION

VIDEO SENDER wirelessly transmit Video/Audio signals over-the-air to the receiving TV within a range more than 100 ft. It eliminates the problems of extending or re-connecting cables. It is just like a mini-broadcasting station.

To transmit recorded pictures from your VCR, you simply connect the Video/Audio output connections on your VCR directly to the Video/Audio input connections on the VIDEO SENDER.

To transmit live video pictures, you plug in any standard Video Camera (color or B/W) to the Video/Audio input connections on VIDEO SENDER.

Now, the pictures (live or recorded) can be viewed on any television tuned to the indicated channel.

FEATURES

1. Ensure the stability and accuracy of the frequency transmission by crystal oscillation.
2. Uses low-level radio waves, no radio license is needed.
3. Uses for power supply: AC power adaptor (10V 13.8V DC).
4. Versatile application: Can be applied to (1) VCR (2) Video Camera (3) Micro-Computer/TV game (4) TV monitoring system.

LOCATION AND USE OF CONTROLS

- A. Power on/off switch
- B. 12 Volts DC socket for AC adaptor
- C. Audio in
- D. Video in
- E. Power on lamp
- F. Antenna
- G. Video control
- H. Audio control
- I. Sound frequency control

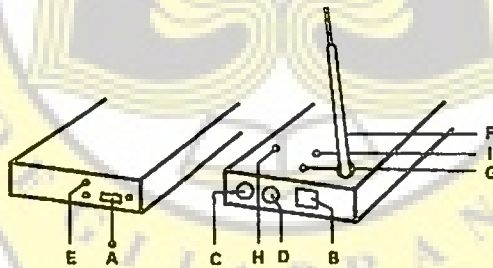
ANTENNA FOR RECEIVING TV SET

- 20ft-50ft use rod antenna
- 50-100 use UHF indoor antenna
- 100ft onwards use UHF outdoor antenna

OPERATING PROCEDURE AND CAUTION

1. Tune receiving TV set to the indicated channel.
2. Insert antenna to VIDEO SENDER until a click is heard; with flexible antenna model, extend antenna to the full.
3. Connect VIDEO SENDER either to Video Camera or VCR by enclosed cables as illustrated.
4. If you plug one end of the cables into Audio (Video) jack of your VCR or Video Camera, the other end for VIDEO SENDER. Audio (Video) jack must be of the same color.
5. Switch on the power switch on VIDEO SENDER.
6. Switch on your VCR or Video Camera.
7. Fine tune your TV set or adjust TV antenna to optimize sharp picture and clear sound.

8. In a few cases, it may be necessary to adjust Audio/Video controls of VIDEO SENDER to optimize picture display.
9. Remove AC adaptor from wall outlet and VIDEO SENDER which is not in use for a prolonged period.



SPECIFICATION

1. Frequency Range: Standard Channel
2. Number of Channel: Single channel
3. Power Input: AC adaptor (10V 13.8V DC)
4. Power Consumption: 10mA maximum
5. Antenna Impedance: 50 Ohms
6. Video Input: 1.0 Volt peak to peak/75 Ohms
7. Audio Input: 0.1-1.0 Volts RMS/600 Ohms
8. Weight: 0.45 lb
9. Dimensions: 70m/m(W) x 20m/m(H) x 123m/m(L)

Bentuk Robot :

