

DAFTAR PUSTAKA

- [1] W. Wu, Y. Liu, Y. He, H. S.-H. Chung, M. Liserre, and F. Blaabjerg, "Damping Methods for Resonances Caused by LCL-Filter-Based Current-Controlled Grid-Tied Power Inverters: An Overview," *IEEE Transactions on Industrial Electronics*, vol. 64, no. 9, pp. 7402–7413, 2017, doi: <https://doi.org/10.1109/TIE.2017.2714143>.
- [2] D. Pan, X. Ruan, X. Wang, H. Yu, and Z. Xing, "Analysis and Design of Current Control Schemes for LCL-Type Grid-Connected Inverter Based on a General Mathematical Model," *IEEE Trans Power Electron*, vol. 32, no. 6, pp. 4395–4410, 2017, doi: <https://doi.org/10.1109/TPEL.2016.2602219>.
- [3] A. Chatterjee and K. B. Mohanty, "Current control strategies for single phase grid integrated inverters for photovoltaic applications-a review," *Renewable and Sustainable Energy Reviews*, vol. 92, pp. 554–569, 2018, doi: <https://doi.org/10.1016/j.rser.2018.04.115>.
- [4] S. Anand, S. K. Gundlapalli, and B. G. Fernandes, "Transformer-Less Grid Feeding Current Source Inverter for Solar Photovoltaic System," *IEEE Transactions on Industrial Electronics*, vol. 61, no. 10, pp. 5334–5344, 2014, doi: <https://doi.org/10.1109/TIE.2014.2300038>.
- [5] H.-J. Lee, S. Jung, and S.-K. Sul, "A Current Controller Design for Current Source Inverter-Fed AC Machine Drive System," *IEEE Trans Power Electron*, vol. 28, no. 3, pp. 1366–1381, 2013, doi: <https://doi.org/10.1109/TPEL.2012.2208985>.
- [6] A. Singh and B. Mirafzal, "An Efficient Grid-Connected Three-Phase Single-Stage Boost Current Source Inverter," *IEEE Power and Energy Technology Systems Journal*, vol. 6, no. 3, pp. 142–151, 2019, doi: <https://doi.org/10.1109/JPETS.2019.2929952>.
- [7] M. Morawiec, "The adaptive backstepping control of permanent magnet synchronous motor supplied by current source inverter," *IEEE Trans Industr Inform*, vol. 9, no. 2, pp. 1047–1055, May 2013, doi: <https://doi.org/10.1109/TII.2012.2223478>.
- [8] B. Wu and M. Narimani, "PWM Current Source Inverters," in *High-Power Converters and AC Drives*, IEEE, 2017, pp. 225–256. doi: <https://doi.org/10.1002/9781119156079.ch10>.
- [9] E. Lorenzani, F. Immovilli, G. Migliazza, M. Frigieri, C. Bianchini, and M. Davoli, "CSI7: A Modified Three-Phase Current-Source Inverter for Modular Photovoltaic Applications," *IEEE Transactions on Industrial Electronics*, vol. 64, no. 7, pp. 5449–5459, 2017, doi: <https://doi.org/10.1109/TIE.2017.2674595>.
- [10] E. Pouresmaeil, M. F. Akorede, D. Montesinos-Miracle, O. Gomis-Bellmunt, and J. C. Trujillo Caballero, "Hysteresis current control technique of VSI for compensation of grid-connected unbalanced loads," *Electrical Engineering*, vol. 96, no. 1, pp. 27–35, 2014, doi: <https://doi.org/10.1007/s00202-012-0273-3>.

- [11] F. Gao, "An Enhanced Single-Phase Step-Up Five-Level Inverter," *IEEE Trans Power Electron*, vol. 31, no. 12, pp. 8024–8030, Dec. 2016, doi: <https://doi.org/10.1109/TPEL.2016.2555934>.
- [12] N. Sandeep, J. S. M. Ali, U. R. Yaragatti, and K. Vijayakumar, "A Self-Balancing Five-Level Boosting Inverter with Reduced Components," *IEEE Trans Power Electron*, vol. 34, no. 7, pp. 6020–6024, Jul. 2019, doi: <https://doi.org/10.1109/TPEL.2018.2889785>.
- [13] M. Saedian, S. M. Hosseini, and J. Adabi, "A five-level step-up module for multilevel inverters: Topology, modulation strategy, and implementation," *IEEE J Emerg Sel Top Power Electron*, vol. 6, no. 4, pp. 2215–2226, Dec. 2018, doi: <https://doi.org/10.1109/JESTPE.2018.2819498>.
- [14] O. Abdel-Rahim, M. Orabi, and M. E. Ahmed, "Buck-boost interleaved inverter for grid connected photovoltaic system," in *PECon2010 - 2010 IEEE International Conference on Power and Energy*, 2010, pp. 63–68. doi: <https://doi.org/10.1109/PECON.2010.5697558>.
- [15] T. Muhammed Nishad and K. Muhammedali Shafeeque, "A novel single stage buck boost inverter for photovoltaic applications," in *2016 International Conference on Electrical, Electronics, and Optimization Techniques (ICEEOT)*, 2016, pp. 3067–3071. doi: <https://doi.org/10.1109/ICEEOT.2016.7755265>.
- [16] U. A. Khan and J. W. Park, "Full-Bridge Single-Inductor-Based Buck-Boost Inverters," *IEEE Trans Power Electron*, vol. 36, no. 2, pp. 1909–1920, Feb. 2021, doi: <https://doi.org/10.1109/TPEL.2020.3011462>.
- [17] Y. Tang, X. Dong, and Y. He, "Active buck-boost inverter," *IEEE Transactions on Industrial Electronics*, vol. 61, no. 9, pp. 4691–4697, 2014, doi: <https://doi.org/10.1109/TIE.2013.2293694>.
- [18] Q. Huang, A. Q. Huang, R. Yu, P. Liu, and W. Yu, "High-Efficiency and High-Density Single-Phase Dual-Mode Cascaded Buck-Boost Multilevel Transformerless PV Inverter with GaN AC Switches," *IEEE Trans Power Electron*, vol. 34, no. 8, pp. 7474–7488, Aug. 2019, doi: <https://doi.org/10.1109/TPEL.2018.2878586>.
- [19] M. Vijeh, M. Rezanejad, E. Samadaei, and K. Bertilsson, "A General Review of Multilevel Inverters Based on Main Submodules: Structural Point of View," *IEEE Trans Power Electron*, vol. 34, no. 10, pp. 9479–9502, Oct. 2019, doi: <https://doi.org/10.1109/TPEL.2018.2890649>.
- [20] A. Gaikwad and P. A. Arbune, "International Conference on Automatic Control & Dynamic Optimization Techniques (ICACDOT 2016): 9th & 10th September 2016," in *2016 International Conference on Automatic Control and Dynamic Optimization Techniques (ICACDOT)*, 2016, pp. 179–182. doi: <https://doi.org/10.1109/ICACDOT.2016.7877574>.
- [21] Suroso, A. N. Aziz, and T. Noguchi, "Five-level PWM inverter with a single DC power source for DC-AC power conversion," *International Journal of Power Electronics and Drive Systems*, vol. 8, no. 3, pp. 1230–1237, Sep. 2017, doi: <http://doi.org/10.11591/ijpeds.v8.i3.pp1212-1219>.
- [22] M. Farhadi-Kangarlu and Milad Gavipankeh Marangalu, "Five-Level Single-DC Source Inverter with Adjustable DC-Link Voltage," in *Electrical*

- Engineering (ICEE), Iranian Conference*, Mashad, Iran, 2018, pp. 1017–1021. doi: <https://doi.org/10.1109/ICEE.2018.8472625>.
- [23] A. Kahwa, H. Obara, and Y. Fujimoto, “Design of 5-level reduced switches count H-bridge multilevel inverter,” in *2018 IEEE 15th International Workshop on Advanced Motion Control (AMC)*, 2018, pp. 41–46. doi: <https://doi.org/10.1109/AMC.2019.8371060>.
- [24] L. H. Pratomo, F. Danang Wijaya, and E. Firmansyah, “A Simple Strategy of Controlling a Balanced Voltage Capacitor in Single Phase Five-Level Inverter Keyword: Five level inverter Power semiconductor Pulse width modulation Renewable energy system Voltage balancing,” *International Journal of Power Electronics and Drive System (IJPEDS)*, vol. 6, no. 1, pp. 160–167, 2015, doi: <https://doi.org/10.11591/ijpeds.v6.i1.pp160-167>
- [25] A. Krishna R and L. P. Suresh, “A brief review on multi level inverter topologies,” in *2016 International Conference on Circuit, Power and Computing Technologies (ICCPCT)*, 2016, pp. 1–6, doi: <https://doi.org/10.1109/ICCPCT.2016.7530373>
- [26] U. Chourasia and P. Tiwari, “Voltage Harmonic Reduction using Open Loop Controlled Multilevel Inverter for Photovoltaic Application: A Review,” *International Research Journal of Engineering and Technology*, vol. 07, no. 11, pp. 464–468, 2020, Accessed: Mar. 16, 2023. [Online]. Available: <https://www.irjet.net/archives/V7/i11/IRJET-V7I1175.pdf>
- [27] R. O. Anurangi, A. S. Rodrigo, and U. Jayatunga, “Effects of high levels of harmonic penetration in distribution networks with photovoltaic inverters,” in *2017 IEEE International Conference on Industrial and Information Systems (ICIIS)*, 2017, pp. 1–6. doi: <https://doi.org/10.1109/ICIINFS.2017.8300335>.