Braking to Maximize the Charging Current into the Battery in SRM Drive," in *Proceedings - 2019 International Seminar on Application for Technology of Information and Communication: Industry 4.0: Retrospect, Prospect, and Challenges, iSemantic 2019*, 2019, pp. 523–527, doi: 10.1109/ISEMANTIC.2019.8884342.

https://ieeexplore.ieee.org/document/8884342

[21] A. P. Khedkar and P. S. Swami, "Comparative study of asymmetric bridge and split AC supply converter for switched reluctance motor," in 2017

International Conference on Computation of Power, Energy Information and Communication (ICCPEIC), 2017, pp. 522–526, doi:

10.1109/ICCPEIC.2017.8290421.

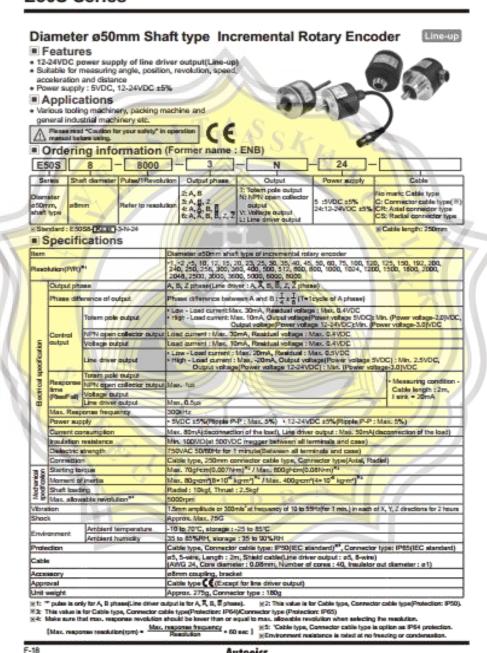
https://ieeexplore.ieee.org/document/8290421

O. Ellabban and H. Abu-Rub, "Switched reluctance motor converter topologies: A review," in 2014 IEEE International Conference on Industrial Technology (ICIT), 2014, pp. 840–846, doi: 10.1109/ICIT.2014.6895009.

https://ieeexplore.ieee.org/document/6895009

LAMPIRAN

E50S Series



Autonics



dsPIC30F4011/4012

dsPIC30F4011/4012 Enhanced Flash 16-bit Digital Signal Controller

Note: This data sheet summarizes features of this group of dsPIC30F devices and is not intended to be a complete reference source. For more information on the CPU, peripherals, register descriptions and general device functionality, refer to the dsPIC30F Family Reference Manual (DS70046). For more information on the device instruction set and programming, refer to the dsPIC30F Programmer's Reference Manual (DS70030).

High Performance Modified RISC CPU:

- Modified Harvard architecture
- C compiler optimized instruction set architecture with flexible addressing modes
- 84 base instructions
- · 24-bit wide instructions, 16-bit wide data path
- 48 Kbytes on-chip Flash program space (16K Instruction words)
- 2 Kbytes of on-chip data RAM
- 1 Kbytes of non-volatile data EEPROM
- Up to 30 MIPs operation:
- DC to 40 MHz external clock input
- 4 MHz-10 MHz oscillator input with PLL active (4x, 8x, 16x)
- 30 interrupt sources
- 3 external interrupt sources
- 8 user selectable priority levels for each interrupt source
- 4 processor trap sources
- 16 x 16-bit working register array

DSP Engine Features:

- Dual data fetch
- Accumulator write back for DSP operations
- Modulo and Bit-Reversed Addressing modes
- Two, 40-bit wide accumulators with optional saturation logic
- 17-bit x 17-bit single cycle hardware fractional/ integer multiplier
- All DSP instructions single cycle
- ± 16-bit single cycle shift

Peripheral Features:

- · High current sink/source I/O pins: 25 mA/25 mA
- · Timer module with programmable prescaler:
 - Five 16-bit timers/counters; optionally pair 16-bit timers into 32-bit timer modules
- · 16-bit Capture input functions
- 16-bit Compare/PWM output functions
- 3-wire SPI™ modules (supports 4 Frame modes)
- I²C™ module supports Multi-Master/Slave mode and 7-bit/10-bit addressing
- 2 UART modules with FIFO Buffers
- 1 CAN modules, 2.0B compliant

Motor Control PWM Module Features:

- 6 PWM output channels
- Complementary or Independent Output modes
- Edge and Center Aligned modes
- 3 duty cycle generators
- Dedicated time base
- Programmable output polarity
- Dead-time control for Complementary mode
- Manual output control
- Trigger for A/D conversions

Quadrature Encoder Interface Module Features:

- Phase A, Phase B and Index Pulse input
- 16-bit up/down position counter
- Count direction status
- Position Measurement (x2 and x4) mode
- Programmable digital noise filters on inputs
- Alternate 16-bit Timer/Counter mode
- Interrupt on position counter rollover/underflow

© 2005 Microchip Technology Inc.

Preliminary

DS70135C-page 1

TOSHIBA TLP250

TOSHIBA Photocoupler GaAlAs Ired & Photo-IC

TLP250

Transistor Inverter

Inverter For Air Conditionor

IGBT Gate Drive

Power MOS FET Gate Drive

The TOSHIBA TLP250 consists of a GaAlAs light emitting diode and a integrated photodetector.

This unit is 8-lead DIP package.

TLP250 is suitable for gate driving circuit of IGBT or power MOS FET.

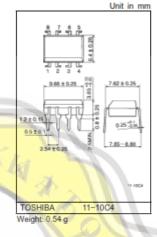
- Input threshold current: IF=5mA(max.).
- Supply current (ICC): 11mA(max.)
- Supply voltage (VCC): 10-35V Output current (IO): ±1.5A (max.)
- Switching time (tpLH/tpHL): 1.5µs(max.)
- Isolation voltage: 2500Vrms(min.)
- UL recognized: UL1577, file No.E67349
- Option (D4) type

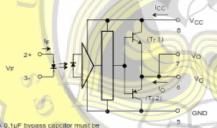
Schmatic

VDE approved: DIN VDE0884/06.92, certificate No.76823 Maximum operating insulation voltage: 630Vpk Highest permissible over voltage: 4000VPK

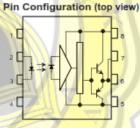
(Note) When a VDE0884 approved type is needed, please designate the "option (D4)" Creepage distance: 6.4mm(min.)

Clearance: 6.4mm(min.)





ected between pin 8 and 5 (See Note 5)



- 1: N.C. 2: Anode
- N.C.
- 5 : GND 6 : VO (Output)
- Vo

Truth Table

	_	Tr1	Tr2
Input	On	On	ОП
LED	Off	Off	On

2004-06-25



Current Transducer LA 55-P/SP1

For the electronic measurement of currents: DC, AC, pulsed..., with galvanic isolation between the primary circuit (high power) and the secondary circuit (electronic circuit).





I_{PN} = 50 A



Electrical data

I _{PN}	Primary nominal curr	ent rms		50			Α
I _{PM}	Primary current, measuring range		-	0 ± 100			A
R_{M}	Measuring resistance		$T_{\Delta} = 7$	T ₀ = 70°C T		Γ _Δ = 85°C	
			R _{M min}	R _{M max}	R _{M min}	R _{M max}	
	with ± 12 V	@ ± 50 A max	0	215	0	210	Ω
	1/6.	@ ± 100 A max	0	35	0	30	Ω
	with ± 15 V	@ ± 50 A max	0	335	30	330	Ω
		@ ± 100 A max	0	95	30	90	Ω
I _{SN}	Secondary nominal c	urrent rms		25		No.	mΑ
I _{SN}	Conversion ratio			1:3	2000	MI	
V _c	Supply voltage (± 5	%)		± 13	215		V
Ic	Current consumption			10	@ ± 15	5 V) + I _s	mA

Accuracy - Dynamic performance data

			No.
х	Accuracy @ I _{PN} , T _A = 25°C @ ± 15 V (± 5 %)	± 0.65	%
11	@ ± 12 15 V (± 5 %)	± 0.90	%
٤,	Linearity error	< 0.15	%
,		Typ Max	
I _o	Offset current @ I _p = 0, T _A = 25°C	± 0.10	mA
I _{OM}	Magnetic offset current 1) @ Ip = 0 and specified R _M ,		
Om	after an overload of 3 x I	± 0.15	mA
I _{OT}	Temperature variation of I - 25°C + 85°C	± 0.05 ± 0.30	mA
0.	- 40°C 25°C	± 0.10 ± 0.50	mA
t _{ra}	Reaction time to 10 % of I _{PN} step	< 500	ns
t,	Response time 2) to 90 % of Ippl step	<1	μs
di/dt	di/dt accurately followed	> 200	A/µs
BW	Frequency bandwidth (- 1 dB)	DC 200	kHz
10.1		-	

General data

T _A	Ambient operating temperature		- 40 + 85	°C
T _s	Ambient storage temperature		- 40 + 90	°(
R _s	Secondary c <mark>oil resistance</mark>	@ T _A = 70°C	145	2
-		@ T ₄ = 85°C	150	2
m	Mass	4 1 .	18	(
	Standards	UA	EN 50178: 199	7

Notes: 1) Result of the coercive field of the magnetic circuit 2) With a di/dt of 100 A/µs.

Features

- Closed loop (compensated) current transducer using the Hall effect
- Printed circuit board mounting
- Insulated plastic case recognized according to UL 94-V0.

Special features

- = 0 .. ± 100 A = 1 : 2000.

Advantages

- Excellent accuracy
- Very good linearity
- Low temperature drift
- Optimized response time
 Wide frequency bandwidth
- No insertion losses
- High immunity to external interference
- Current overload capability.

Applications

- AC variable speed drives and
- servo motor drives
- Static converters for DC motor drives
- Battery supplied applications
- Uninterruptible Power Supplies
- Switched Mode Power Supplies (SMPS)
- Power supplies for welding applications.

Application domain

Industrial.

Page 1/3

090519/13

LEM reserves the right to carry out modifications on its transducers, in order to improve them, without prior notice

www.lem.com

PAPER NAME

TA-18.F1.0027.doc

WORD COUNT CHARACTER COUNT 6519 Words 41996 Characters

PAGE COUNT FILE SIZE
36 Pages 282.5KB

SUBMISSION DATE REPORT DATE

Oct 14, 2022 9:00 AM GMT+7 Oct 14, 2022 9:01 AM GMT+7

• 12% Overall Similarity

The combined total of all matches, including overlapping sources, for each database.

- 10% Internet database
- Crossref database
- 6% Submitted Works database
- 1% Publications database
- Crossref Posted Content database

Summary