DAFTAR PUSTAKA

- [1] .N. Rao and J. Nakka, "Three-Phase Four-Leg Four-Wire Topology in High Power Factor Converter addressing the problem of unbalanced source currents," 2018 IEEE 8th Power India International Conference (PIICON), 2018, pp. 1-6, doi: 10.1109/POWERI.2018.8704390I. S. Jacobs and C. P. Bean, "Fine particles, thin films and exchange anisotropy," in Magnetism, vol. III, G. T. Rado and H. Suhl, Eds. New York: Academic, 1963, pp. 271–350.
- [2] A. A. Khan, U. A. Khan, H. F. Ahmed, H. Cha and S. Ahmed, "Improved NPC Inverters Without Short-Circuit and Dead-Time Issues," in IEEE Transactions on Power Electronics, vol. 37, no. 2, pp. 2180-2190, Feb. 2022, doi: 10.1109/TPEL.2021.3103159.
- [3] Y. Kihara et al., "Harmonics Compensation With Constant DC-Capacitor Voltage-Control-Based Strategy for an Active Power-Line Conditioner in Three-Phase Four-Wire Distribution Feeders," 2020 23rd International Conference on Electrical Machines and Systems (ICEMS), 2020, pp. 971-976, doi: 10.23919/ICEMS50442.2020.9291139R. Nicole, "Title of paper with only first word capitalized," J. Name Stand. Abbrev., in press.
- [4] Z. Zhu and W. Chen, "Zero sequence voltage and current control in four-wire gridsfed by grid-forming inverters," in CSEE Journal of Power and Energy Systems, doi: 10.17775/CSEEJPES.2020.03230M.

- [5] D. I. Brandao, F. E. G. Mendes, R. V. Ferreira, S. M. Silva and I. A. Pires, "Active and Reactive Power Injection Strategies for Three-Phase Four-Wire Inverters During Symmetrical/Asymmetrical Voltage Sags," in IEEE Transactions on Industry Applications, vol. 55, no. 3, pp. 2347-2355, May-June 2019, doi: 10.1109/TIA.2019.2893135.
- [6] A. F. Wibisono and L. H. Pratomo, "Implementation of Voltage Control in Single-Phase Full Bridge Inverter Using One-Leg Plus Hysteresis Controller," 2021 International Conference on Technology and Policy in Energy and Electric Power (ICT-PEP), 2021, pp. 11-16, doi: 10.1109/ICT-PEP53949.2021.9600987.
- [7] L. H. Pratomo and C. Tjokro, "Hardware Implementation of an Asymmetrical 11-Level Inverter with Automatic Boost Charge Control in PV Applications," 2019 International Seminar on Application for Technology of Information and Communication (iSemantic), 2019, pp. 336-341, doi: 10.1109/ISEMANTIC.2019.8884256.
- [8] Z. Lin, X. Ruan, L. Jia, W. Zhao, H. Liu and P. Rao, "Optimized Design of the Neutral Inductor and Filter Inductors in Three-Phase Four-Wire Inverter With Split DC-Link Capacitors," in IEEE Transactions on Power Electronics, vol. 34, no. 1, pp. 247-262, Jan. 2019, doi: 10.1109/TPEL.2018.2812278.
- [9] Y. Fu et al., "Imbalanced Load Regulation Based on Virtual Resistance of A Three-Phase Four-Wire Inverter for EV Vehicle-to-Home Applications," in IEEE

Transactions on Transportation Electrification, vol. 5, no. 1, pp. 162-173, March 2019, doi: 10.1109/TTE.2018.2874357.

- [10] H. Moon, J. Lee, J. Lee and K. Lee, "MPC-SVM method with subdivision strategy for current ripples reduction and neutral-point voltage balance in threelevel inverter," 2017 IEEE Energy Conversion Congress and Exposition (ECCE), 2017, pp. 191-196, doi: 10.1109/ECCE.2017.8095780.
- [11] T. Qanbari and B. Tousi, "Single-Source Three-Phase Multilevel Inverter Assembled by Three-Phase Two-Level Inverter and Two Single-Phase Cascaded H-Bridge Inverters," in IEEE Transactions on Power Electronics, vol. 36, no. 5, pp. 5204-5212, May 2021, doi: 10.1109/TPEL.2020.3029870.
- [12] A. Tcai, S. Pugliese and M. Liserre, "Discontinuous Modulation of Interleaved Parallel NPC Inverters with Reduced Circulating Current," 2019 IEEE Energy Conversion Congress and Exposition (ECCE), 2019, pp. 4403-4408, doi: 10.1109/ECCE.2019.8912174.
- [13] S. He, D. Zhou, X. Wang and F. Blaabjerg, "Grid Voltage Sensorless Control of Three-Phase LCL Grid-Connected Inverters Using Multisampled Current," 2020 IEEE 9th International Power Electronics and Motion Control Conference (IPEMC2020-ECCE Asia), 2020, pp. 2002-2006, doi: 10.1109/IPEMC-ECCEAsia48364.2020.9367836.
- [14] S. Jena, N. Tiwary, C. K. Panigrahi and P. K. Sahu, "Performance Improvement of Grid Integrated Voltage Source Inverter Using Different Hysteresis Current

Controllers," 2020 IEEE 7th Uttar Pradesh Section International Conference on Electrical, Electronics and Computer Engineering (UPCON), 2020, pp. 1-6, doi: 10.1109/UPCON50219.2020.9376436.

- [15] L. H. Pratomo and S. Aditya, "Design and Implementation of One-Leg and PI Control Single-Phase H-Bridge Current Regulated Inverter," 2021 International Seminar on Application for Technology of Information and Communication (iSemantic), 2021,pp.376-382,doi: 10.1109/iSemantic52711.2021.9573251.
- [16] S. K. Singh and S. Ghatak Choudhuri, "A conflict in control strategy of voltage and current controllers in Multi-Modular single-phase UPS inverters system," 2017
 10th International Symposium on Advanced Topics in Electrical Engineering (ATEE), 2017, pp. 631-636, doi: 10.1109/ATEE.2017.7905042.
- [17] A. S. Mohamad, "Matrix Inverter: A Multilevel Inverter Based on Matrix Converter Switch Matrix," 2020 IEEE Electric Power and Energy Conference (EPEC), 2020, pp. 1-5, doi: 10.1109/EPEC48502.2020.9320121.
- [18] J. Baek, S. -e. Kim and S. Kwak, "Predictive control method for load current of single-phase voltage source inverters," 2015 IEEE Applied Power Electronics Conference and Exposition (APEC),2015, pp. 2256-2260, doi: 10.1109/APEC.2015.7104663.
- [19] S. Fan, Y. Yu, Y. Zhang and H. Yang, "Multi-mode Synchronized PWM Schemes for three-level NPC Inverter," 2019 22nd International Conference on

Electrical Machines and Systems (ICEMS), 2019, pp. 1-5, doi: 10.1109/ICEMS.2019.8921689.

- [20] S. K. Singh and S. Ghatak Choudhuri, "A conflict in control strategy of voltage and current controllers in Multi-Modular single-phase UPS inverters system," 2017
 10th International Symposium on Advanced Topics in Electrical Engineering (ATEE), 2017, pp. 631-636, doi: 10.1109/ATEE.2017.7905042.
- [21] Rajiv K. Varma, "CONTROL COORDINATION OF SMART PV INVERTERS," in Smart Solar PV Inverters with Advanced Grid Support Functionalities, IEEE, 2022, pp.369-429, doi: 10.1002/9781119214236.ch8.
- [22] Rajiv K. Varma, "EMERGING TRENDS WITH SMART SOLAR PV INVERTERS," in Smart Solar PV Inverters with Advanced Grid Support Functionalities, IEEE, 2022, pp.431-464, doi: 10.1002/9781119214236.ch9.
- [23] Rajiv K. Varma, "MODELING AND CONTROL OF THREE-PHASE SMART PV INVERTERS," in Smart Solar PV Inverters with Advanced Grid Support Functionalities , IEEE, 2022, pp.73-106, doi: 10.1002/9781119214236.ch3.
- [24] T. Qanbari and B. Tousi, "Single-Source Three-Phase Multilevel Inverter Assembled by Three-Phase Two-Level Inverter and Two Single-Phase Cascaded H-Bridge Inverters," in IEEE Transactions on Power Electronics, vol. 36, no. 5, pp. 5204-5212, May 2021, doi: 10.1109/TPEL.2020.3029870.

- B. K. Gupta, K. R. Sekhar and A. I. Gedam, "Balanced Per-Phase Sequential Switching to Suppress Circulating Current in Grid Connected Modular Solar Inverters," 2019 8th International Conference on Renewable Energy Research and Applications (ICRERA), 2019, pp. 686-691, doi: 10.1109/ICRERA47325.2019.8996838.
- [26] H. Jiang, Y. Zhang, L. Huang and J. Liu, "An Inverter Input Current Closed-loop Control Scheme for IPMSM Drives Fed by Electrolytic Capacitorless Converter," 2020 IEEE 9th International Power Electronics and Motion Control Conference (IPEMC2020-ECCE Asia), 2020, pp. 1795-1799, doi: 10.1109/IPEMC-ECCEAsia48364.2020.9367930.
- [27] G. d. O. Assuncao and I. Barbi, "Method for Deriving Transformerless Common-Ground Voltage Source Inverter Topologies," in IEEE Transactions on Power Electronics, doi: 10.1109/TPEL.2022.3162771.
- [28] "IEEE Draft Standard for Interconnection and Interoperability of Inverter-Based Resources (IBR) Interconnecting with Associated Transmission Electric Power Systems," in IEEE P2800/D6.1, June 2021, vol., no., pp.1-197, 11 June 2021.
- [29] H. Jiang, Y. Zhang, L. Huang and J. Liu, "An Inverter Input Current Closedloop Control Scheme for IPMSM Drives Fed by Electrolytic Capacitorless Converter," 2020 IEEE 9th International Power Electronics and Motion Control

Conference (IPEMC2020-ECCE Asia), 2020, pp. 1795-1799, doi: 10.1109/IPEMC-ECCEAsia48364.2020.9367930.

[30] A. Panda, G. Dyanamina and R. K. Singh, "MATLAB Simulation of Space Vector Pulse Width Modulation for 3-level NPC Inverter and 2-level Inverter," 2021 International Conference on Sustainable Energy and Future Electric Transportation (SEFET), 2021, pp. 1-5, doi: 10.1109/SeFet48154.2021.9375668.

