

## DAFTAR PUSTAKA

- [1] C. Tjokro and L. H. Pratomo, "Design and Simulation of an Asymmetrical 11-Level Inverter for Photovoltaic Applications," *Proc. - 2018 5th Int. Conf. Inf. Technol. Comput. Electr. Eng. ICITACEE 2018*, pp. 93–98, 2018.
- [2] M. Müller, R. Bründlinger, O. Arz, W. Miller, J. Schulz, and G. Lauss, "PV-off-grid hybrid systems and MPPT charge controllers, a state of the art analyses," *Energy Procedia*, vol. 57, no. December, pp. 1421–1430, 2014.
- [3] J. U. Lim, H. W. Kim, K. Y. Cho, and J. H. Bae, "Stand-alone microgrid inverter controller design for nonlinear, unbalanced load with output transformer," *Electron.*, vol. 7, no. 4, pp. 1–16, 2018.
- [4] E. A. Nugroho, "Implementasi Inverter Sebagai Pengendali Motor Induksi Tiga Fasa Dengan Metode Space Vector Pulse Width Modulation (Svpwm)," *Simetris J. Tek. Mesin, Elektro dan Ilmu Komput.*, vol. 7, no. 2, p. 597, 2016.
- [5] S. Daher, J. Schmid, and F. L. M. Antunes, "Multilevel inverter topologies for stand-alone PV systems," *IEEE Trans. Ind. Electron.*, vol. 55, no. 7, pp. 2703–2712, 2008.
- [6] G. Mohapatra, "Multilevel inverter ; A review MULTILEVEL INVERTER ; A REVIEW," no. April, 2018.
- [7] K. N. V. Prasad, G. R. Kumar, T. V. Kiran, and G. S. Narayana, "Comparison of Different Topologies of Cascaded," pp. 2–7, 2013.
- [8] A. Y, "Design and Simulation of Single-Phase Five-Level Symmetrical

- Cascaded H-Bridge Multilevel Inverter with Reduces Number of Switches,” *J. Electr. Electron. Syst.*, vol. 07, no. 04, 2018.
- [9] S. Modugu, “Spwm techniques in five level inverter,” no. December, 2018.
- [10] S. Khadse, R. Mendole, and A. Pandey, “A 5-Level Single Phase Flying Capacitor Multilevel Inverter,” *Int. Res. J. Eng. Technol.*, vol. 4, no. 2, pp. 348–352, 2017.
- [11] L. Heru Pratomo, F. D. Danang Wijaya, and E. Firmansyah, “A simple strategy of controlling a balanced voltage capacitor in single phase five-level inverter,” *Int. J. Power Electron. Drive Syst.*, vol. 6, no. 1, pp. 160–167, 2015.
- [12] G. Fernandez and V. Krishnasamy, “A New Symmetric & Asymmetric Multilevel Inverter Topology with Reduced Maximum Blocking Voltage Switches,” no. July, 2018.
- [13] K. Ganesh and U. Rao, “Performance of Symmetrical and Asymmetrical Multilevel Inverters,” *Citeseer*, vol. 2, no. 4, pp. 2293–2302, 2012.
- [14] E. Irmak, I. Colak, O. Kaplan, and N. Guler, “Design and application of a novel zero-crossing detector circuit,” *Int. Conf. Power Eng. Energy Electr. Drives*, no. May, 2011.
- [15] A. Algaddafi, K. Elnaddab, A. Al Ma’Mari, and A. N. Esgiar, “Comparing the performance of bipolar and unipolar switching frequency to drive DC-AC Inverter,” *Proc. 2016 Int. Renew. Sustain. Energy Conf. IRSEC 2016*, no. October 2017, pp. 680–685, 2017.
- [16] M. R. Banaei and E. Salary, “Asymmetric cascaded multi-level inverter: A

- solution to obtain high number of voltage levels,” *J. Electr. Eng. Technol.*, vol. 8, no. 2, pp. 316–325, 2013.
- [17] K. S. Rao and R. Mishra, “Comparative study of P, PI and PID controller for speed control of VSI-fed induction motor,” vol. 2, no. 2, pp. 2740–2744, 2014.
- [18] H. P. Mosfet and M. Units, “Irfp260N Irfp260N,” pp. 1–9.
- [19] M. Fezari, “Get more Power in your projects with ‘ Arduino Due ’ Introduction to Arduino Due,” no. September, 2018.
- [20] I. Inverter, I. For, A. Conditioner, and T. Table, “Tlp250,” pp. 1–7, 2019.
- [21] F. I. Isolated, “B \_ S-1W & B \_ D-1W Series,” pp. 3–5, 2009.
- [22] L. Components, “Voltage Transducer LV 25-P I PN = 10 mA V PN = 10 .. 500 V Electrical data,” *Data Sheet*, no. 02, pp. 1–2, 1997.
- [23] F. Inverter and L. H. Pratomo, “One Leg Control Strategy in Single-Phase,” *2019 Int. Symp. Electr. Electron. Eng.*, no. Isee, pp. 0–4, 2019.
- [24] D. Santoso and L. H. Pratomo, “Design and simulation of an asymmetrical control strategy in single- phase five-levels inverter Design and simulation of an asymmetrical control strategy in single-phase five-levels inverter,” 2020.
- [25] S. Podder, M. Biswas, and Z. R. Khan, “A modified PWM technique to improve total harmonic distortion of multilevel inverter,” *Proc. 9th Int. Conf. Electr. Comput. Eng. ICECE 2016*, no. January 2018, pp. 515–518, 2017.