

LAMPIRAN

TOSHIBA

TLP250

TOSHIBA Photocoupler GaAlAs Ired & Photo-IC

TLP250

Transistor Inverter
 Inverter For Air Conditioner
 IGBT Gate Drive
 Power MOS FET Gate Drive

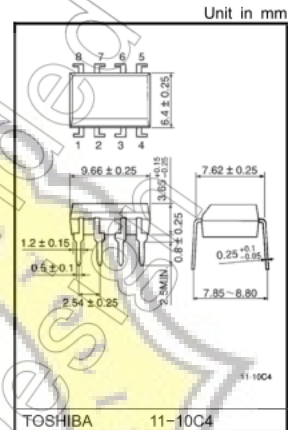
The TOSHIBA TLP250 consists of a GaAlAs light emitting diode and a integrated photodetector.
 This unit is 8-lead DIP package.
 TLP250 is suitable for gate driving circuit of IGBT or power MOS FET.

- Input threshold current: $I_F=5\text{mA}(\text{max.})$
- Supply current (I_{CC}): $11\text{mA}(\text{max.})$
- Supply voltage (V_{CC}): $10\text{--}35\text{V}$
- Output current (I_O): $\pm 1.5\text{A}(\text{max.})$
- Switching time (t_{pLH}/t_{pHL}): $0.5\mu\text{s}(\text{max.})$
- Isolation voltage: $2500\text{V}_{\text{rms}}(\text{min.})$
- UL recognized: UL1577, file No.E67349

• Option(D4)

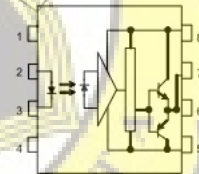
VDE Approved : DIN EN60747-5-2
 Maximum Operating Insulation Voltage : 850V_{PK}
 Highest Permissible Over Voltage : 4000V_{PK}

(Note):When a EN60747-5-2 approved type is needed,
 Please designate "Option(D4)"



Weight: 0.54 g (typ.)

Pin Configuration (top view)

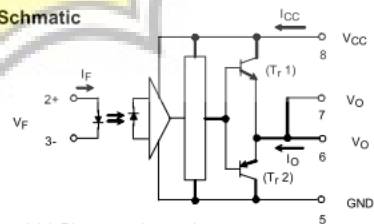


- 1 : N.C.
- 2 : Anode
- 3 : Cathode
- 4 : N.C.
- 5 : GND
- 6 : V_O (Output)
- 7 : V_O
- 8 : V_{CC}

Truth Table

Input LED	Tr1		Tr2	
	On	Off	On	Off
On	On	Off	Off	On
Off	Off	Off	On	Off

Schematic



A 0.1 μF bypass capacitor must be connected between pin 8 and 5 (See Note 5).



dsPIC30F4011/4012

dsPIC30F4011/4012 Enhanced Flash 16-bit Digital Signal Controller

Note: This data sheet summarizes features of this group of dsPIC30F devices and is not intended to be a complete reference source. For more information on the CPU, peripherals, register descriptions and general device functionality, refer to the *dsPIC30F Family Reference Manual* (DS70046). For more information on the device instruction set and programming, refer to the *dsPIC30F Programmer's Reference Manual* (DS70030).

High Performance Modified RISC CPU:

- Modified Harvard architecture
- C compiler optimized instruction set architecture with flexible addressing modes
- 84 base instructions
- 24-bit wide instructions, 16-bit wide data path
- 48 Kbytes on-chip Flash program space (16K instruction words)
- 2 Kbytes of on-chip data RAM
- 1 Kbytes of non-volatile data EEPROM
- Up to 30 MIPS operation:
 - DC to 40 MHz external clock input
 - 4 MHz-10 MHz oscillator input with PLL active (4x, 8x, 16x)
- 30 interrupt sources
 - 3 external interrupt sources
 - 8 user selectable priority levels for each interrupt source
 - 4 processor trap sources
- 16 x 16-bit working register array

DSP Engine Features:

- Dual data fetch
- Accumulator write back for DSP operations
- Modulo and Bit-Reversed Addressing modes
- Two, 40-bit wide accumulators with optional saturation logic
- 17-bit x 17-bit single cycle hardware fractional/integer multiplier
- All DSP instructions single cycle
- \pm 16-bit single cycle shift

Peripheral Features:

- High current sink/source I/O pins: 25 mA/25 mA
- Timer module with programmable prescaler:
 - Five 16-bit timers/counters; optionally pair 16-bit timers into 32-bit timer modules
- 16-bit Capture input functions
- 16-bit Compare/PWM output functions
- 3-wire SPI™ modules (supports 4 Frame modes)
- I²C™ module supports Multi-Master/Slave mode and 7-bit/10-bit addressing
- 2 UART modules with FIFO Buffers
- 1 CAN modules, 2.0B compliant

Motor Control PWM Module Features:

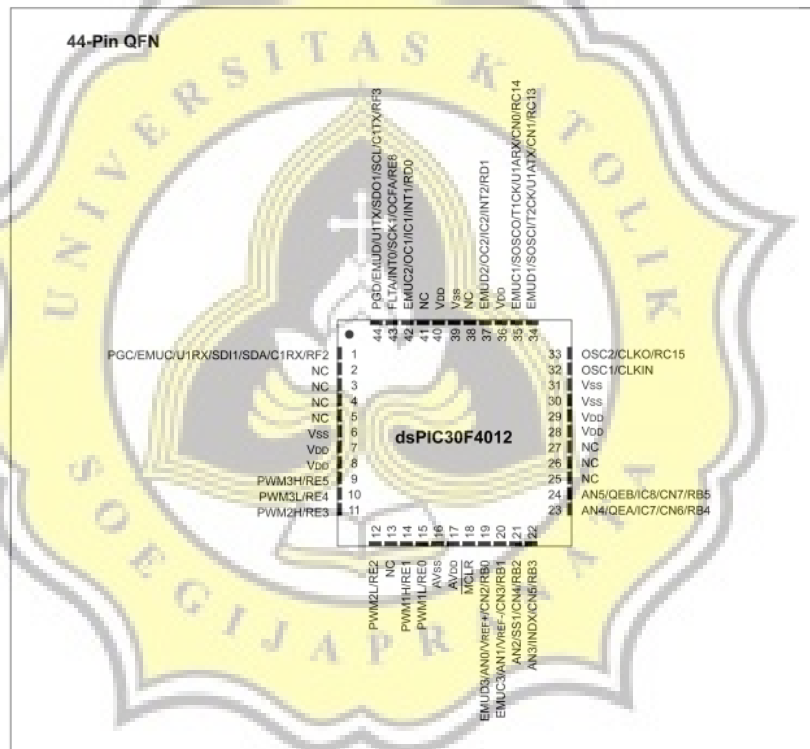
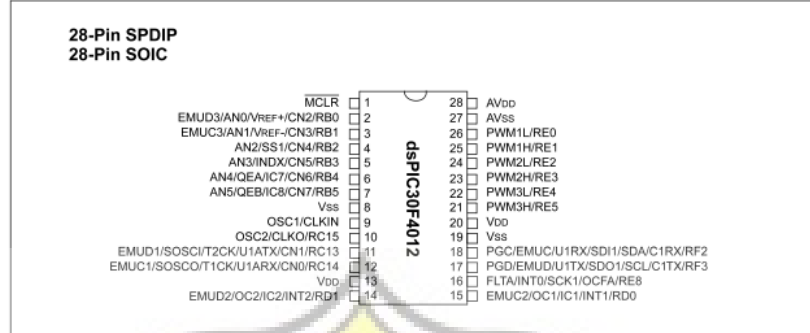
- 6 PWM output channels
 - Complementary or Independent Output modes
 - Edge and Center Aligned modes
- 3 duty cycle generators
- Dedicated time base
- Programmable output polarity
- Dead-time control for Complementary mode
- Manual output control
- Trigger for A/D conversions

Quadrature Encoder Interface Module Features:

- Phase A, Phase B and Index Pulse input
- 16-bit up/down position counter
- Count direction status
- Position Measurement (x2 and x4) mode
- Programmable digital noise filters on inputs
- Alternate 16-bit Timer/Counter mode
- Interrupt on position counter rollover/underflow

dsPIC30F4011/4012

Pin Diagrams (Continued)



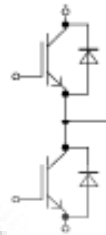
Technische Information / Technical Information

IGBT-Module
IGBT-modules

FF100R12KS4



62mm C-Serien Modul mit schnellem IGBT2 für hochfrequentes Schalten
62mm C-Series module with the fast IGBT2 for high-frequency switching



$V_{CES} = 1200V$
 $I_{CSM} = 100A / I_{CM} = 200A$

Typische Anwendungen

- Anwendungen mit hohen Schaltfrequenzen
- Medizinische Anwendungen
- Motorantriebe
- Anwendungen für Resonanz-Umrichter
- Servoantriebe
- USV-Systeme

Typical Applications

- High Frequency Switching Application
- Medical Applications
- Motor Drives
- Resonant Inverter Applications
- Servo Drives
- UPS Systems

Elektrische Eigenschaften

- Hohe Kurzschlussrobustheit, selbstlimitierender Kurzschlussstrom
- Niedrige Schaltverluste
- Sehr große Robustheit
- V_{CES} mit positivem Temperaturkoeffizienten

Electrical Features

- High Short Circuit Capability, Self Limiting Short Circuit Current
- Low Switching Losses
- Unbeatable Robustness
- V_{CES} with positive Temperature Coefficient

Mechanische Eigenschaften

- Gehäuse mit CTI > 400
- Große Luft- und Kriechstrecken
- Isolierte Bodenplatte
- Kupferbodenplatte
- Standardgehäuse

Mechanical Features

- Package with CTI > 400
- High Creepage and Clearance Distances
- Isolated Base Plate
- Copper Base Plate
- Standard Housing

Module Label Code

Barcode Code 128



Content of the Code

Content of the Code	Digit
Module Serial Number	1 - 5
Module Material Number	6 - 11
Production Order Number	12 - 19
Datecode (Production Year)	20 - 21
Datecode (Production Week)	22 - 23

DMX - Code



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